The Crowdsourcing Compiler

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## modern programming

- specify broad control and data structures
- don't worry about:
  - memory management/allocation/reuse
  - primary/secondary/tertiary
  - loop optimization
  - parallelization, cluster management
- don't need to know much about how computers "really" work

## imagine a high-level programming language...

- much like those of today
- but with built-in functionality for social computation
- e.g classifying objects, making predictions/decisions, optimization/search
- perhaps even in the physical world (e.g. taskrabbit, uber)

## ...whose compiler would decide:

- human or machine?
- sequential or parallel?
- incentives: payment (subject to budget), entertainment, prestige, purpose,...
- individuals or groups?
- structure, organization, communication
- coverage/overlap
- toy problem: collective short-term memorization
- sample instantiation: Emery Berger's AutoMan at UMass; others?



				QUADRUPL WITH S SCLS034F - DE	SN54HC132, E POSITIVE-NA CHMITT-TRIGO CEMBER 1982 - REVISE	SN74HC132 AND GATES ER INPUTS D NOVEMBER 2004
<ul> <li>Wide</li> <li>Outp</li> <li>Low</li> <li>Typic</li> <li>±4-m</li> <li>Low</li> <li>Oper Trans</li> <li>Temp Leve</li> <li>High</li> <li>Same</li> <li>description</li> <li>Each becau thresh signals function</li> <li>These can be and st</li> </ul>	<ul> <li>Wide Operating Voltage Range of 2 V to 6 V</li> <li>Outputs Can Drive Up To 10 LSTTL Loads</li> <li>Low Power Consumption, 20-µA Max I<sub>CC</sub></li> <li>Typical t<sub>pd</sub> = 14 ns</li> <li>±4-mA Output Drive at 5 V</li> <li>Low Input Current of 1 µA Max</li> <li>Operation From Very Slow Input Transitions</li> <li>Temperature-Compensated Threshold Levels</li> <li>High Noise Immunity</li> <li>Same Pinouts as 'HC00</li> <li>Each circuit functions as a NAND gate, but because of the Schmitt action, it has different input threshold levels for positive- and negative-going signals. The 'HC132 devices perform the Boolean function Y = A • B or Y = A + B in positive logic.</li> <li>These circuits are temperature compensated and can be triggered from the slowest of input ramps and still give clean jitter-free output signals.</li> </ul>			SN54HC132D R V PACKAGE SN74HC132D, DB, N, NS, OR PW PACKAGE (TOP VIEW) 1A $\begin{bmatrix} 1 & & & & & & & & & & & & & & & & & & $		
		OR	DERING INF	DRMATION		
	TA	PACKA	GET	ORDERABLE PART NUMBER	TOP-SIDE MARKING	
		PDIP – N	Tube of 25	SN74HC132N	SN74HC132N	
	-40°C to 85°C	SOIC – D	Tube of 50	SN74HC132D	HC132	
			Reel of 2500	SN74HC132DR		
			Reel of 250	SN74HC132DT		
		SOP - NS	Reel of 2000	SN74HC132NSR	HC132	
		SSOP – DB	Reel of 2000	SN74HC132DBR	HC132	
		TSSOP – PW	Tube of 90	SN74HC132PW	HC132	
			Reel of 2000	SN74HC132PWR		
			Reel of 250	SN74HC132PWT		
		CDIP – J	Tube of 25	SNJ54HC132J	SNJ54HC132J	
	-55°C to 125°C	CFP – W	Tube of 150	SNJ54HC132W	SNJ54HC132W	
	L .	LCCC - FK	Tube of 55	SNJ54HC132FK	SNJ54HC132FK	
available at www.ti.com/sc/package.						

## hard questions

- what should the "components" look like?
- what should their "operating characteristics" or specs look like?
- will heterogeneity of "hardware" kill this whole idea?
- is the whole idea just too creepy to contemplate?