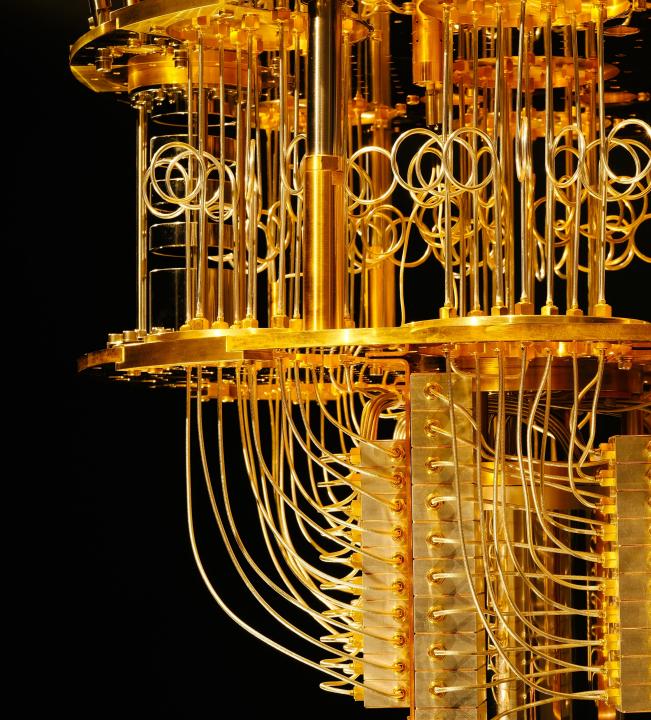
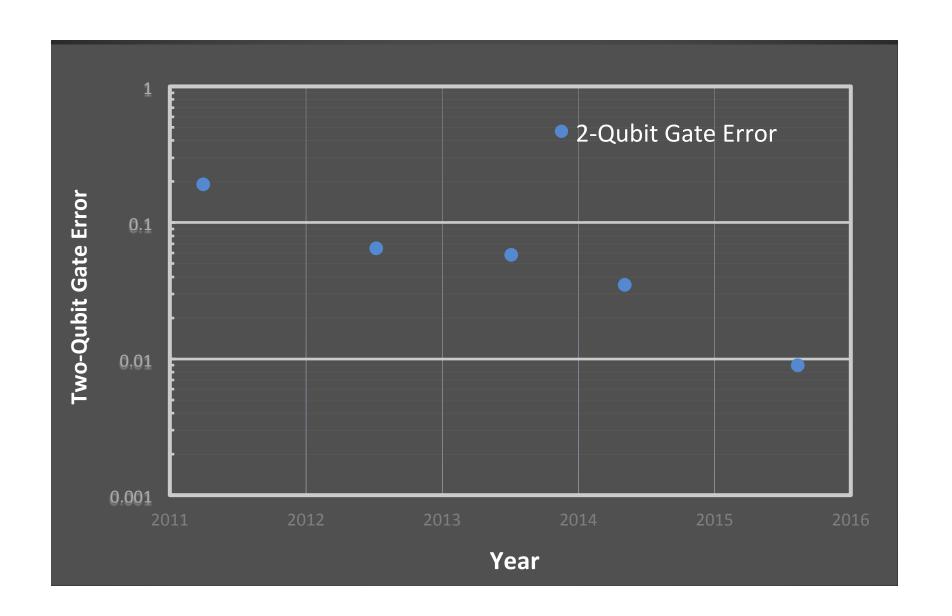
Quantum Toolchains

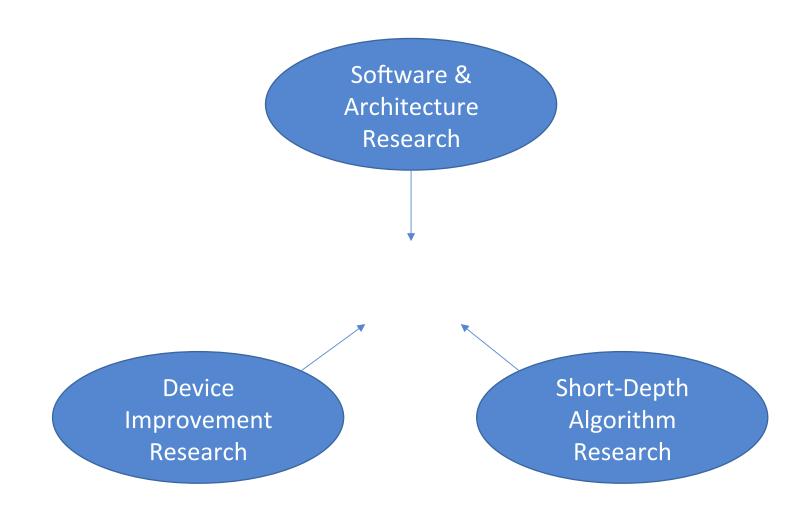
Ali Javadi-Abhari IBM Research



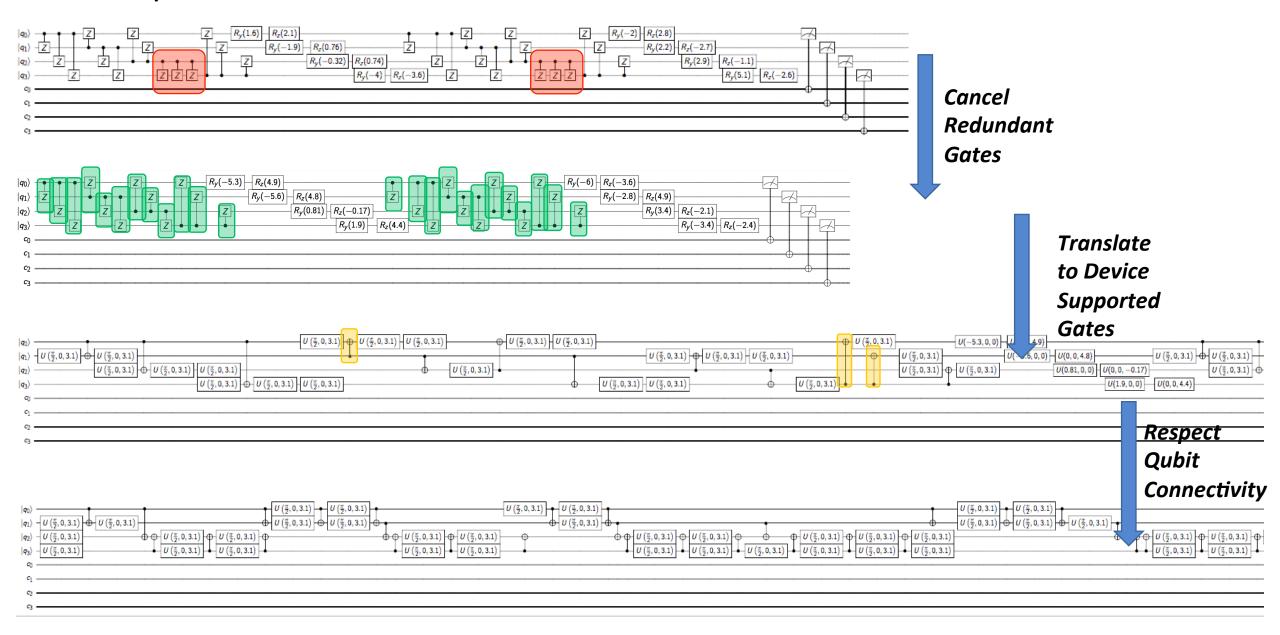
Progress in Superconducting Qubits



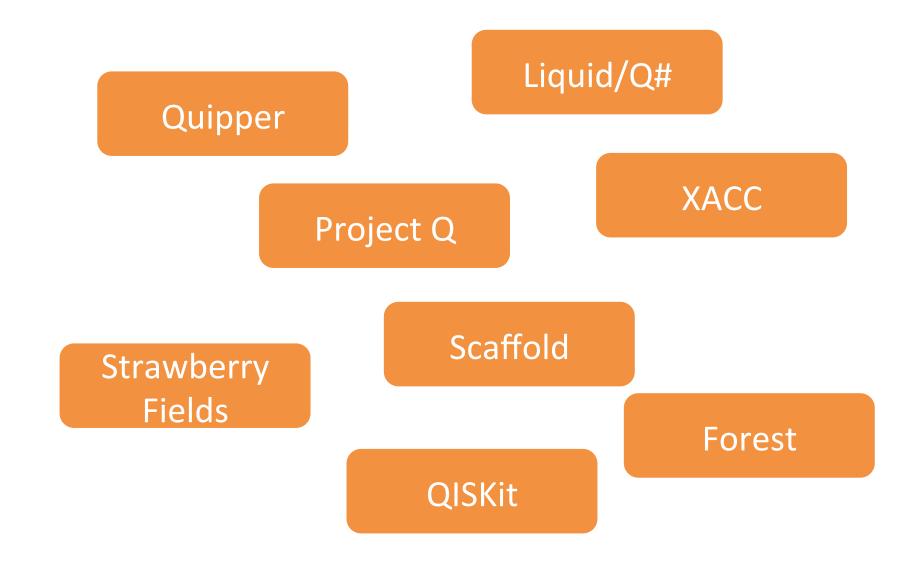
Computer Science's Role?



An Example Use of a Toolchain



Existing Toolchains





1. Errors

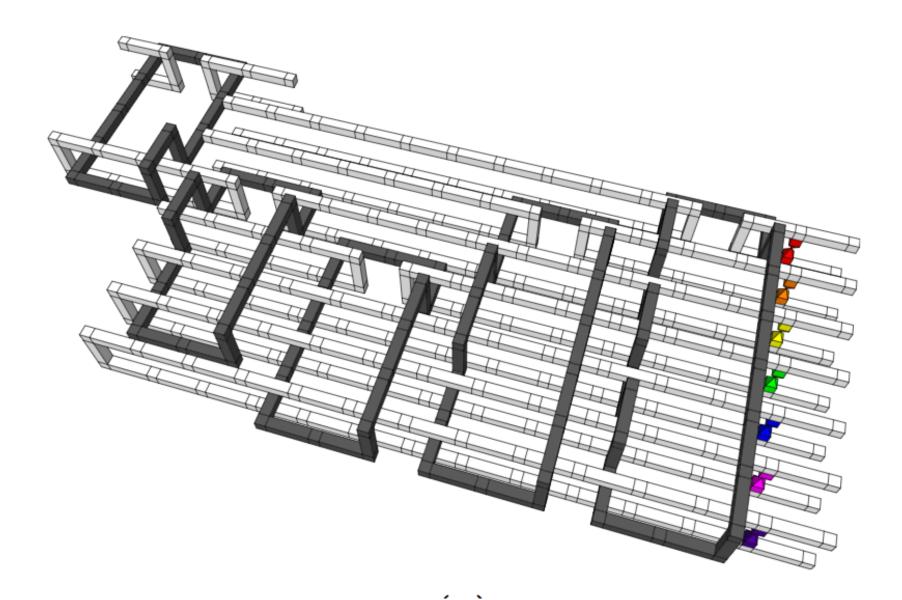
Long term:

- Quantum Error Correction to detect/correct general errors (so expensive that we will basically build an error correcting machine, and computation will be a byproduct)

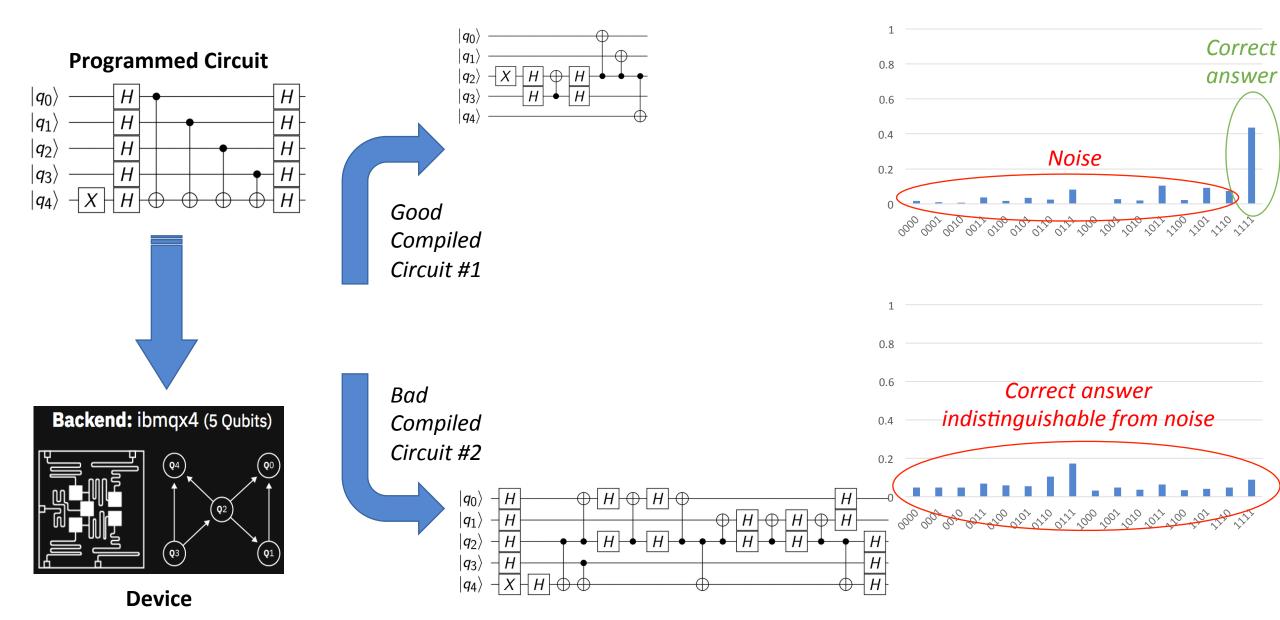
Near term:

- Heavy circuit optimization to prevent error accumulation
- Error mitigation techniques

Compiling for Future Machines



Compiling for Near Term Machines

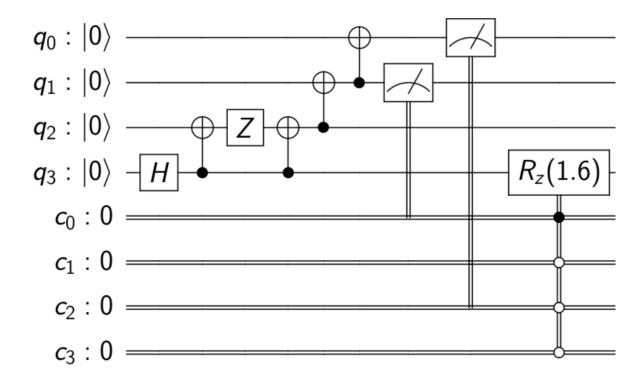


2. Extreme Latency Sensitivity

Limited coherence:

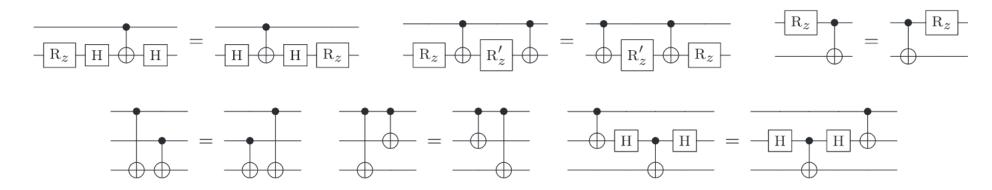
limits measure + fast feedback

Compilers need to know something about the physical controller layout.



3. Circuit Synthesis, Optimization, Scheduling

- Automatic synthesis of reversible circuits
- Asymptotically efficient, reduce constant factors
- Gate identity libraries
- Parallelism is affected by gate commutation relations



4. Adaptive Compilation

- Qubit/gate characteristics widely vary over time. Adapt.

| | Q0 | Q1 | Q2 | Q3 | Q4 |
|---|---------------|-------------------|-------------------------------------|---------------------------------|-------------------|
| Frequency (GHz) | 5.24 51.20 | 5.31 54.00 | 5.35 46.80 | 5.41 39.50 | 5.19 52.50 |
| T1 (μs) T2 (μs) | 14.00 | 62.50 | 68.40 | 18.10 | 28.20 |
| Gate error (10^{-3}) Readout error (10^{-2}) | 0.94 5.00 | 2.92 17.00 | 1.46 6.60 | 3.95 12.70 | 1.20 7.40 |
| MultiQubit gate error (10^{-2}) | | CX1_0 2.46 | cx2_0 3.01 cx2_1 3.08 | CX3_2 10.14 CX3_4 8.75 | CX4_2 5.56 |

5. Limited Power BudgetsHeat dissipation at cryogenic tempratures is a challenge.

