Valentina Salapura

Senior Fellow, Computing,
AMD

Awards and Honors and Year Received

- Fellow, IEEE, 2011 ACM Gordon Bell Prize 2006 ACM Distinguished Speaker Member, IFIP WG 10.3 for Concurrent Systems since 2009, chair since 2016

Involvement in CRA Activities

- Yes, I was a Distinguished Lecturer for CRA-W in 2008, and participated in several CRA-W workshops as a panelist and speaker.

Other Relevant Experience

- I am the founder and (Co-)Chair of series of Career Workshop for Inclusion and Diversity in Computer Architecture (previously Career Workshop for Women and Minorities in Computer Architecture), held since 2014.
- I was a Program (Co-)Chair or General (Co-)Chair for several conferences and workshops in cloud computing and parallel processing, such as ISCA 2022, PACT 2010, and HiPEAC 2014; responsible for the overall conference organization, finances, and legal and tax compliance.
- I was an organizer of the Grace Hopper Conference for the Industry Advisory Board and Workshops and Presentations Committee during several years.
- member of ACM/IEEE Ken Kennedy Award Committee (2014-2016), chair in 2015.
- IBM Fran Allen Fellowship and IBM PhD Fellowship Committee (2008-2013).

Research Interests

- Large-scale, parallel system architecture, design, analysis, and optimization
- Complex distributed systems architecture

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Personal Statement

I am one of the top technical leaders and researchers in the computer industry today and one of the most prolific innovators. Over several decades of my career, I worked on building the fastest supercomputer, cloud computing and distributed computing, advancing the state of the art and mentoring a large number of colleagues. My passion is to increase diversity in the computer science and engineering.